

Concurrency Theory

Lecture 22: Timed Modelling & Conclusions

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Lehrstuhl für Informatik 2
(Software Modeling and Verification)



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<http://www-i2.informatik.rwth-aachen.de/i2/ct13/>

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Wanted: Software Engineering HiWis

- What we offer: work in
 - ESA project HASDEL
 - Hardware-Software Dependability for Launchers
 - successor of COMPASS project (compass.informatik.rwth-aachen.de)
 - goal: enhance COMPASS for rocket design validation
 - EU project D-MILS
 - Dependability and Security of Distributed Information and Communication Infrastructures
 - design and implementation of high-level specification language
- What we expect: prospective candidates
 - like formal methods (model checking, program/model transformations)
 - program efficiently (Python)
 - work 9–19 hrs/week
- Contact: Thomas Noll (noll@cs.rwth-aachen.de)



- 1 Real-Time Reactive Systems
- 2 CCS with Time Delays
- 3 Syntax of Timed CCS
- 4 Semantics of Timed CCS
- 5 Handling Parallel Composition
- 6 Miscellaneous

So far: “Qualitative” Modelling

- **Algebraic language** (CCS) for syntactic description of concurrent systems
- Meaning given by **LTSs** that define dynamic behaviour of process terms
- **Structural operational semantics** for mapping CCS processes to LTSs
- Notions of **behavioural equivalence** (trace equivalence, bisimilarity) for comparing process behaviours
- **Modal logics** (HML) to specify desired system properties
- Petri Nets as model of **true concurrency** with partial-order semantics

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 - Petri Nets as model of **true concurrency** with partial-order semantics
- ⇒ very abstract (if any) notion of **time**:
logical order of computation steps

Example 22.1 (Real-time reactive systems)

- brake systems and airbags in cars
- plant controls
- mobile phones
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Real-time requirements

The correct behaviour of a real-time system does not only depend on the **logical order** in which events are performed but also on their **timing**.

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Real-time requirements

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Example 22.2 (Untimed vs. timed)

- Untimed: “if the car crashes, eventually the airbag will be inflated”
- Timed: “if the car crashes, the airbag must be inflated within 50 milliseconds”

Extensive research work on **formal methods for real-time systems**:

- **Modelling**

- extensions of CCS: Timed CCS (TCCS; Yi 1990), Temporal Process Algebra (Hennessy/Regan 1995), Temporal CCS (Moller/Tofts 1990)
- extensions of other untimed process algebras (ACP, CSP)
- timed automata (Alur/Dill 1990)

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- **Requirement specification**

- HML with time (Laroussinie et al. 1990)
- extensions of LTL: Timed Propositional Temporal Logic (TPTL; Alur/Henzinger 1994), Metric Temporal Logic (MTL; Koymans 1990)
- extension of CTL: Timed Computation Tree Logic (TCTL; Alur et al. 1993)

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- timed behavioural equivalences (timed trace equivalence, timed bisimilarity)
- abstraction of timed automata via regions and zones

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- Here: **syntax and semantics of Timed CCS**

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Example 22.3 (Light switch)

- ➊ If the switch is off, and is pressed once, then the light will turn on.
- ➋ If the switch is pressed again “soon” after the light was turned on, the light becomes brighter. Otherwise, the light is turned off by the next button press.
- ➌ The light is also turned off by a button press when it is bright.

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 - in CCS: *Off = press.Light*
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- ② If the switch is pressed again “soon” after the light was turned on, the light becomes brighter. Otherwise, the light is turned off by the next button press.
 - in CCS: $Light = press.Bright + \tau.press.Off$
 - but: does not properly capture the “soon” requirement
 - rather: system may internally choose to switch off light after next button press (after “timeout” action τ)
- ③ The light is also turned off by a button press when it is bright.
 - in CCS: $Bright = press.Off$

Modelling with time delays

$$Light = press.Bright + \varepsilon(1.5).\tau.press.Off$$

- passage of time viewed as “action” performed by system
- specified by new prefixing operator $\varepsilon(d).P$ where $d \in \mathbb{R}_{\geq 0}$ gives amount of time that needs to elapse before $P \in Prc$ is enabled
- thus: “soon” interpreted as “within 1.5 time units”
- use of τ is crucial here: must be performed when enabled (details later)

Definition 22.4 (Timed labelled transition system)

A **timed labelled transition system (TLTS)** is a triple $(S, Lab, \longrightarrow)$ consisting of

- a set S of **states**
- a set $Lab = Act \cup \mathbb{R}_{\geq 0}$ of **labels**
 - **actions** $a \in Act$
 - **time delays** $d \in \mathbb{R}_{\geq 0}$
- a **transition relation** $\longrightarrow \subseteq S \times Lab \times S$ (written $s \xrightarrow{\lambda} s'$)

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Additional requirements:

- **time additivity**: if $s \xrightarrow{d} s'$ and $0 \leq d' \leq d$, then $s \xrightarrow{d'} s'' \xrightarrow{d-d'} s'$ for some $s'' \in S$
- **self-reachability without delay**: $s \xrightarrow{0} s$ for each $s \in S$
- **time determinism**: if $s \xrightarrow{d} s'$ and $s \xrightarrow{d} s''$, then $s' = s''$

Example 22.5 (Timed labelled transition system)

$$(S, Lab, \longrightarrow)$$

where

- $S = \mathbb{R}_{\geq 0}$
- $Lab = \{a\} \cup \mathbb{R}_{\geq 0}$
- $\xrightarrow{a} = \{(5, 0)\}$
- for all $d \in \mathbb{R}_{\geq 0}$: $\xrightarrow{d} = \{(s, s + d) \mid s \in \mathbb{R}_{\geq 0}\}$

(diagram on the board)

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- Let Pid be a set of process identifiers.
- The set Prc of process expressions is defined by the following syntax:

$P ::=$	nil	(inaction)
	$\alpha.P$	(prefixing)
	$P_1 + P_2$	(choice)
	$P_1 \parallel P_2$	(parallel composition)
	$P \setminus L$	(restriction)
	$P[f]$	(relabelling)
	C	(process call)
	$\varepsilon(d).P$	(time delay)

where $\alpha \in Act$, $d \in \mathbb{R}_{\geq 0}$, $L \subseteq A$, $C \in Pid$, and $f : Act \rightarrow Act$ such that $f(\tau) = \tau$ and $f(\bar{a}) = \overline{f(a)}$ for each $a \in A$.

Definition 22.6 (continued)

- A (recursive) process definition is an equation system of the form

$$(C_i = P_i \mid 1 \leq i \leq k)$$

where $k \geq 1$, $C_i \in \text{Pid}$ (pairwise distinct), and $P_i \in \text{Prc}$ (with process identifiers from $\{C_1, \dots, C_k\}$).

- An occurrence of a process identifier $C \in \text{Pid}$ in an expression $P \in \text{Prc}$ is guarded if it occurs within a subexpression of P of the form $\lambda.Q$ where $\lambda \in \text{Act}$ or $\lambda = \varepsilon(d)$ for some $d > 0$
- A process expression/definition is guarded if all occurrences of process identifiers are guarded

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Conventions:

- Processes P and $\varepsilon(0).P$ will not be distinguished
- All process definitions have to be guarded

Example 22.7

① $(a.C_1 + (C_2 \parallel b.C_3) + C_1) \parallel (\varepsilon(4.2).(C_4 \parallel \text{nil}) + \varepsilon(1.2).C_3)$

- first occurrence of C_1 is guarded, second unguarded
- occurrence of C_2 is unguarded
- both occurrences of C_3 are guarded
- occurrence of C_4 is guarded
- overall expression is unguarded

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② $\text{Off} = \text{press.Light}$

$\text{Bright} = \text{press.Off}$

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is guarded

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Definition 22.8 (Semantics of TCCS – action transitions; cf. Def. 2.4)

A process definition $(C_i = P_i \mid 1 \leq i \leq k)$ determines the TLTS $(Prc, Lab, \longrightarrow)$ whose transitions can be inferred from the following rules $(P, P', Q, Q' \in Prc, \alpha \in Act, \lambda \in A \cup \bar{A}, a \in A)$:

$$(\text{Del}) \frac{P \xrightarrow{\alpha} P'}{\varepsilon(0).P \xrightarrow{\alpha} P'}$$

$$(\text{Act}) \frac{}{\alpha.P \xrightarrow{\alpha} P}$$

$$(\text{Sum}_1) \frac{P \xrightarrow{\alpha} P'}{P + Q \xrightarrow{\alpha} P'}$$

$$(\text{Sum}_2) \frac{Q \xrightarrow{\alpha} Q'}{P + Q \xrightarrow{\alpha} Q'}$$

$$(\text{Par}_1) \frac{P \xrightarrow{\alpha} P'}{P \parallel Q \xrightarrow{\alpha} P' \parallel Q}$$

$$(\text{Par}_2) \frac{Q \xrightarrow{\alpha} Q'}{P \parallel Q \xrightarrow{\alpha} P \parallel Q'}$$

$$(\text{Com}) \frac{P \xrightarrow{\lambda} P' \quad Q \xrightarrow{\bar{\lambda}} Q'}{P \parallel Q \xrightarrow{\tau} P' \parallel Q'}$$

$$(\text{Res}) \frac{P \xrightarrow{\alpha} P' \quad (\alpha, \bar{\alpha} \notin L)}{P \setminus L \xrightarrow{\alpha} P' \setminus L}$$

$$(\text{Rel}) \frac{P \xrightarrow{\alpha} P'}{P[f] \xrightarrow{f(\alpha)} P'[f]}$$

$$(\text{Call}) \frac{P \xrightarrow{\alpha} P' \quad (C = P)}{C \xrightarrow{\alpha} P'}$$

Definition 22.8 (Semantics of TCCS – timed transitions)

Additionally for $d, d' \in \mathbb{R}_{\geq 0}$:

$$\begin{array}{ll}
 \text{(tAdd)} \frac{P \xrightarrow{d'} P'}{\varepsilon(d).P \xrightarrow{d+d'} P' \quad (\alpha \neq \tau)} & \text{(tSub)} \frac{(d' \leq d)}{\varepsilon(d).P \xrightarrow{d'} \varepsilon(d-d').P'} \\
 \text{(tAct)} \frac{\alpha.P \xrightarrow{d} \alpha.P}{\alpha.P \xrightarrow{d} \alpha.P} & \text{(tTau)} \frac{}{\tau.P \xrightarrow{0} \tau.P} \\
 \text{(tSum)} \frac{P \xrightarrow{d} P' \quad Q \xrightarrow{d} Q'}{P + Q \xrightarrow{d} P' + Q'} & \text{(tRes)} \frac{P \xrightarrow{d} P'}{P \setminus L \xrightarrow{d} P' \setminus L} \\
 \text{(tRel)} \frac{P \xrightarrow{d} P'}{P[f] \xrightarrow{d} P'[f]} & \text{(tCall)} \frac{P \xrightarrow{d} P' \quad (C = P)}{C \xrightarrow{d} P'}
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 \end{array}$$

Remarks:

- **parallel composition** considered later
- delay transitions do **not resolve non-deterministic choices**
(according to time-determinism property of Definition 22.4)

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 $Bright = press.Off$
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- ① $\text{Light} \xrightarrow{\text{press}} \text{Bright}$
- ② for all $0 \leq d \leq 1.5$: $\text{Light} \xrightarrow{d} \text{press.Bright} + \varepsilon(1.5 - d).\tau.\text{press.Off}$

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 $Light \xrightarrow{1.5} press.Bright + \varepsilon(0).\tau.press.Off$
 $\xrightarrow{\tau} press.Off$
 $\xrightarrow{d'} press.Off$
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(for all $d' \in \mathbb{R}_{\geq 0}$)

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(for all $d' \in \mathbb{R}_{\geq 0}$)

- ④ moreover: $press.Bright + \varepsilon(0).\tau.press.Off \not\xrightarrow{d}$ (for any $d > 0$)
 \implies first alternative only enabled up to time point 1.5

Properties of the Semantics

Lemma 22.10 (cf. Definition 22.4)

- ① *time additivity*: if $P \xrightarrow{d} P'$ and $0 \leq d' \leq d$, then $P \xrightarrow{d'} P'' \xrightarrow{d-d'} P'$ for some $P'' \in \text{Prc}$
- ② *self-reachability without delay*: $P \xrightarrow{0} P$ for each $P \in \text{Prc}$
- ③ *time determinism*: if $P \xrightarrow{d} P'$ and $P \xrightarrow{d} P''$, then $P' = P''$
- ④ *persistency of action transitions*: for all $P, Q \in \text{Prc}$, $\alpha \in \text{Act}$ and $d \in \mathbb{R}_{\geq 0}$, if $P \xrightarrow{\alpha}$ and $P \xrightarrow{d} Q$, then $Q \xrightarrow{\alpha}$

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Proof.

- ① by Rules (tAdd), (tSub) and (tAct)
- ② by Rules (tSub), (tAct) and (tTau) (note that every P is guarded)
- ③ by induction on derivation tree
- ④ by induction on derivation tree

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The Light Switch Example Revisited

Example 22.11

$Off = press.Light$
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 $Light = press.Bright + \varepsilon(1.5).\tau.press.Off$
 $FastUser = \overline{press}.\varepsilon(0.3).\overline{press}.FastUser$

The Light Switch Example Revisited

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$$\begin{aligned} \text{Off} &= \text{press}.\text{Light} \\ \text{Bright} &= \text{press}.\text{Off} \\ \text{Light} &= \text{press}.\text{Bright} + \varepsilon(1.5).\tau.\text{press}.\text{Off} \\ \text{FastUser} &= \overline{\text{press}}.\varepsilon(0.3).\overline{\text{press}}.\text{FastUser} \end{aligned}$$

- Expect immediate synchronisation between *FastUser* and *Off*:
 $(\text{FastUser} \parallel \text{Off}) \setminus \text{press} \xrightarrow{\tau} (\varepsilon(0.3).\overline{\text{press}}.\text{FastUser} \parallel \text{Light}) \setminus \text{press}$

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- Now $\overline{\text{press}}$ -transition only enabled after 0.3 time units, which is also a possible delay for *Light*: $\text{Light} \xrightarrow{0.3} \text{press.Bright} + \varepsilon(1.2).\tau.\text{press.Off}$

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- Therefore expected that whole system can delay:
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 $((\varepsilon(0.3).\overline{\text{press}}.\text{FastUser}) \parallel \text{Light}) \setminus \text{press} \xrightarrow{0.3} ((\overline{\text{press}}.\text{FastUser}) \parallel (\text{press.Bright} + \varepsilon(1.2).\tau.\text{press.Off})) \setminus \text{press}$
- Now another synchronisation should be possible:
 $((\overline{\text{press}}.\text{FastUser}) \parallel (\text{press.Bright} + \varepsilon(1.2).\tau.\text{press.Off})) \setminus \text{press} \xrightarrow{\tau} (\text{FastUser} \parallel \text{Bright}) \setminus \text{press} \quad (*)$

The Light Switch Example Revisited

Example 22.11

$$\begin{aligned} \text{Off} &= \text{press.Light} \\ \text{Bright} &= \text{press.Off} \\ \text{Light} &= \text{press.Bright} + \varepsilon(1.5).\tau.\text{press.Off} \\ \text{FastUser} &= \overline{\text{press}}.\varepsilon(0.3).\overline{\text{press}}.\text{FastUser} \end{aligned}$$

- Expect immediate synchronisation between *FastUser* and *Off*:
 $(\text{FastUser} \parallel \text{Off}) \setminus \text{press} \xrightarrow{\tau} (\varepsilon(0.3).\overline{\text{press}}.\text{FastUser} \parallel \text{Light}) \setminus \text{press}$
- Now $\overline{\text{press}}$ -transition only enabled after 0.3 time units, which is also a possible delay for *Light*: $\text{Light} \xrightarrow{0.3} \text{press.Bright} + \varepsilon(1.2).\tau.\text{press.Off}$
- Therefore expected that whole system can delay:
 $((\varepsilon(0.3).\overline{\text{press}}.\text{FastUser}) \parallel \text{Light}) \setminus \text{press} \xrightarrow{0.3} ((\overline{\text{press}}.\text{FastUser}) \parallel (\text{press.Bright} + \varepsilon(1.2).\tau.\text{press.Off})) \setminus \text{press}$
- Now another synchronisation should be possible:
 $((\overline{\text{press}}.\text{FastUser}) \parallel (\text{press.Bright} + \varepsilon(1.2).\tau.\text{press.Off})) \setminus \text{press} \quad (*) \xrightarrow{\tau} (\text{FastUser} \parallel \text{Bright}) \setminus \text{press}$
- But: both parallel components of $(*)$ can **delay for 1.2 time units**, giving rise to
 $(*) \xrightarrow{1.2} \xrightarrow{\tau} \xrightarrow{\tau} (\text{FastUser} \parallel \text{Off}) \setminus \text{press}$

The Light Switch Example Revisited

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$$\begin{aligned} \text{Off} &= \text{press.Light} \\ \text{Bright} &= \text{press.Off} \\ \text{Light} &= \text{press.Bright} + \varepsilon(1.5).\tau.\text{press.Off} \\ \text{FastUser} &= \overline{\text{press}}.\varepsilon(0.3).\overline{\text{press}}.\text{FastUser} \end{aligned}$$

- Expect immediate synchronisation between *FastUser* and *Off*:
 $(\text{FastUser} \parallel \text{Off}) \setminus \text{press} \xrightarrow{\tau} (\varepsilon(0.3).\overline{\text{press}}.\text{FastUser} \parallel \text{Light}) \setminus \text{press}$
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- Now another synchronisation should be possible:
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- But: both parallel components of $(*)$ can **delay for 1.2 time units**, giving rise to
 $(*) \xrightarrow{1.2} \xrightarrow{\tau} \xrightarrow{\tau} (\text{FastUser} \parallel \text{Off}) \setminus \text{press}$
- How to enforce that intended synchronisation occurs immediately?

The Maximal-Progress Assumption

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If a process is ready to perform an action that is **entirely under its control**, then it will immediately do so **without further delay**.

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In the setting of timed CCS, the only action that is entirely under the control of a process is the τ -action. Therefore:

Maximal-progress assumption for Timed CCS

For each TCCS process $P \in Prc$, if $P \xrightarrow{\tau}$ then $P \not\xrightarrow{d}$ for any $d > 0$.

Definition 22.12 (Semantics of TCCS – timed parallel transitions)

Additionally for $P, P', Q, Q' \in \text{Prc}$ and $d \in \mathbb{R}_{\geq 0}$:

$$(\text{tPar}) \frac{P \xrightarrow{d} P' \quad Q \xrightarrow{d} Q' \quad \text{NoSync}(P, Q, d)}{P \parallel Q \xrightarrow{d} P' \parallel Q'}$$

where predicate $\text{NoSync}(P, Q, d)$ expresses that no synchronisation between P and Q becomes enabled by delaying less than d time units:

For each $0 \leq d' < d$ and $P', Q' \in \text{Prc}$,
if $P \xrightarrow{d'} P'$ and $Q \xrightarrow{d'} Q'$ then $P' \parallel Q' \not\xrightarrow{\tau}$.

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Example 22.13

① $(\varepsilon(0.3).\overline{\text{press}}.\text{FastUser} \parallel \text{Light}) \setminus \text{press} \not\xrightarrow{d}$ for any $d > 0.3$

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Example 22.13

- ① $(\varepsilon(0.3).\overline{\text{press}}.\text{FastUser} \parallel \text{Light}) \setminus \text{press} \not\xrightarrow{d}$ for any $d > 0.3$
- ② $((\overline{\text{press}}.\text{FastUser}) \parallel (\text{press}.\text{Bright} + \varepsilon(1.2).\tau.\text{press}.\text{Off})) \setminus \text{press} \not\xrightarrow{d}$ for any $d > 0$

Example 22.14 (cf. Example 22.11)

$$\begin{aligned} \textit{Off} &= \textit{press}.\textit{Light} \\ \textit{Bright} &= \textit{press}.\textit{Off} \\ \textit{Light} &= \textit{press}.\textit{Bright} + \varepsilon(1.5).\tau.\textit{press}.\textit{Off} \\ \textit{SlowUser} &= \overline{\textit{press}}.\varepsilon(1.7).\overline{\textit{press}}.\textit{SlowUser} \end{aligned}$$

Example 22.14 (cf. Example 22.11)

$$\begin{aligned} \textit{Off} &= \textit{press}.\textit{Light} \\ \textit{Bright} &= \textit{press}.\textit{Off} \\ \textit{Light} &= \textit{press}.\textit{Bright} + \varepsilon(1.5).\tau.\textit{press}.\textit{Off} \\ \textit{SlowUser} &= \overline{\textit{press}}.\varepsilon(1.7).\overline{\textit{press}}.\textit{SlowUser} \end{aligned}$$

- As before:

$$(\textit{SlowUser} \parallel \textit{Off}) \setminus \textit{press} \xrightarrow{\tau} (\varepsilon(1.7).\overline{\textit{press}}.\textit{SlowUser} \parallel \textit{Light}) \setminus \textit{press}$$

Example 22.14 (cf. Example 22.11)

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- As before:

$$(\textit{SlowUser} \parallel \textit{Off}) \setminus \textit{press} \xrightarrow{\tau} (\varepsilon(1.7).\overline{\textit{press}}.\textit{SlowUser} \parallel \textit{Light}) \setminus \textit{press}$$

- Now $\overline{\textit{press}}$ -transition only enabled after 1.7 time units, but \textit{Light} can only delay for at most 1.5 units:

$$\begin{aligned} &((\varepsilon(1.7).\overline{\textit{press}}.\textit{SlowUser}) \parallel \textit{Light}) \setminus \textit{press} \xrightarrow{1.5} \\ &((\varepsilon(0.2).\overline{\textit{press}}.\textit{SlowUser}) \parallel (\textit{press}.\textit{Bright} + \varepsilon(0).\tau.\textit{press}.\textit{Off})) \setminus \textit{press} (*) \end{aligned}$$

Example 22.14 (cf. Example 22.11)

$$\begin{aligned} \text{Off} &= \text{press}.\text{Light} \\ \text{Bright} &= \text{press}.\text{Off} \\ \text{Light} &= \text{press}.\text{Bright} + \varepsilon(1.5).\tau.\text{press}.\text{Off} \\ \text{SlowUser} &= \overline{\text{press}}.\varepsilon(1.7).\overline{\text{press}}.\text{SlowUser} \end{aligned}$$

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- Now $\overline{\text{press}}$ -transition only enabled after 1.7 time units, but Light can only delay for at most 1.5 units:

$$\begin{aligned} &((\varepsilon(1.7).\overline{\text{press}}.\text{SlowUser}) \parallel \text{Light}) \setminus \text{press} \xrightarrow{1.5} \\ &((\varepsilon(0.2).\overline{\text{press}}.\text{SlowUser}) \parallel (\text{press}.\text{Bright} + \varepsilon(0).\tau.\text{press}.\text{Off})) \setminus \text{press} (*) \end{aligned}$$

- Here the right-hand process of $(*)$ can do a τ -action, disabling further delays and thus avoiding the Bright state:

$$(*) \xrightarrow{\tau} ((\varepsilon(0.2).\overline{\text{press}}.\text{SlowUser}) \parallel (\text{press}.\text{Off})) \setminus \text{press}$$

- 1 Real-Time Reactive Systems
- 2 CCS with Time Delays
- 3 Syntax of Timed CCS
- 4 Semantics of Timed CCS
- 5 Handling Parallel Composition
- 6 Miscellaneous

- Written exams:
 - 1st Friday 21 February 11:30 AH 2
 - 2nd Tuesday 25 March 10:00 AH 1

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 - 1st Friday 21 February 11:30 AH 2
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- Teaching in Summer 2014:
 - Seminar Concurrency Theory [Katoen/Noll]
 - Course Advanced Model Checking [Katoen/NN]
 - Course Modelling and Verification of Probabilistic Systems [Katoen/NN]
 - Course Compiler Construction [Noll/NN]