

## Introduction to Model Checking Winter term 2011/2012

### – Series 1 –

Hand in on October 26<sup>th</sup> before the exercise class.

#### Exercise 1

(3 points)

For this exercise we give the following definition:

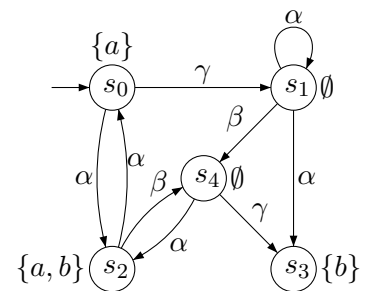
#### Definition 1. Deterministic Transition System

Let  $T = (S, Act, \rightarrow, I, AP, L)$  be a transition system.

- $T$  is called *action-deterministic* if  $|I| \leq 1$  and  $|Post(s, \alpha)| \leq 1$  for all states  $s$  and actions  $\alpha$ .
- $T$  is called *AP-deterministic* if  $|I| \leq 1$  and  $|Post(s) \cap \{s' \in S \mid L(s') = A\}| \leq 1$  for all states  $s$  and  $A \in 2^{AP}$ .

Now let  $TS$  be the transition system depicted on the right.

- Give the formal definition of  $TS$ .
- Specify a finite and an infinite execution of  $TS$ .
- Decide whether  $TS$  is an AP-deterministic or an action-deterministic transition system. Justify your answer!



#### Exercise 2

(1 points)

We are given three (primitive) processes  $P_1, P_2$ , and  $P_3$  with shared integer variable  $x$  and local registers  $r_1, r_2$  and  $r_3$ . The program of process  $P_i$  is as follows:

#### Algorithm 1 Process $P_i$

```

for  $k_i = 1, \dots, 10$  do
  LOAD( $r_i \leftarrow x$ );
  INC( $r_i$ );
  STORE( $r_i \rightarrow x$ );
end for

```

That is,  $P_i$  executes ten times the assignment  $x := x+1$ . The assignment  $x := x+1$  is realized using the three actions LOAD, INC and STORE. Consider now the parallel program:

#### Algorithm 2 Parallel program $P$

```

 $x := 0;$ 
 $P_1 \parallel P_2 \parallel P_3$ 

```

Question: Does  $P$  have an execution that halts with the terminal value  $x = 2$ ?

(4 points)

```

10: loop forever do
    begin
11:     Noncritical section
12:      $(y_i, s) := (1, i);$ 
13:     wait until  $((y_{1-i} = 0) \vee (s \neq i));$ 
14:     Critical section
15:      $y_i := 0$ 
    end.

```

Questions:

- The last two questions may be answered by inspecting the transition system.

**(2 points)**

Figure 1 shows two circuit diagrams,  $C_1$  and  $C_2$ .

$C_1$  is a circuit with inputs  $x$ ,  $r_0$ , and  $r_1$ , and output  $y$ . The circuit is composed of several logic gates: two XOR gates, two AND gates, and two OR gates. The input  $x$  is connected to the first XOR gate and the first AND gate. The input  $r_0$  is connected to the first AND gate and the first OR gate. The input  $r_1$  is connected to the first AND gate and the first OR gate. The output  $y$  is connected to the second XOR gate and the second AND gate.

$C_2$  is a circuit with inputs  $r$  and  $y$ , and output  $y$ . The circuit is composed of a single NOT gate. The input  $r$  is connected to the NOT gate, and the output  $y$  is connected to the NOT gate.

- a) Give the transition system representation  $TS_1$  of the circuit  $C_1$ .

b) Let  $TS_2$  be the transition system of the circuit  $C_2$ . Outline the transition system  $TS_1 \otimes TS_2$ .

*Remark:* The operator  $\otimes$  denotes the synchronous product in which both systems always perform one step synchronously.