

Modeling Concurrent and Probabilistic Systems

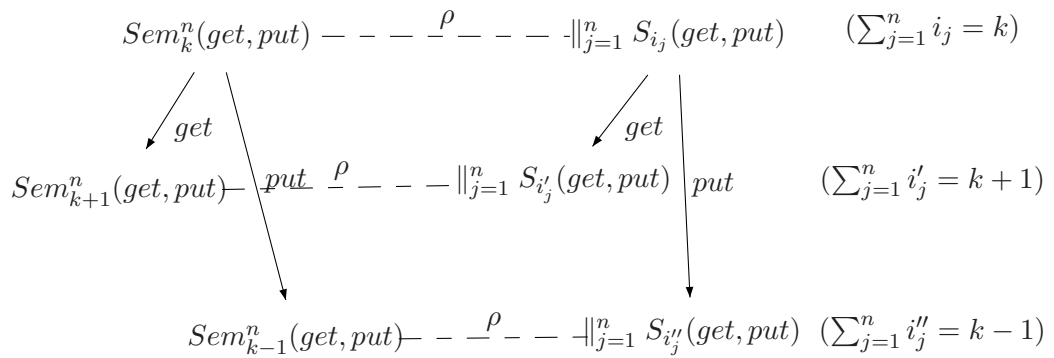
Winter Term 07/08

– Solution 3 –

Exercise 1

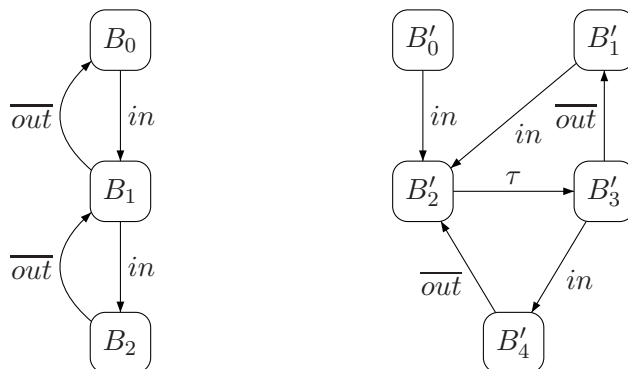
(4 + 1 points)

- a) We show that $\rho = (Sem_0^n(get, put), S^n(get, put)) \cup \{(Sem_k^n(get, put), S_{i_1}(get, put)) \parallel \dots \parallel S_{i_n}(get, put)) \mid 0 \leq k \leq n, \sum_{j=1}^n i_j = k\}$ is a strong bisimulation:



This is the most general case (for $k = 0$, there are no *put*-transitions, and for $k = n$ no *get*-transitions). The states on the right-hand side actually represent all different states for which the summation condition holds – they are equivalent with respect to ρ .

- b) See Example 2.3 for the specifications of the sequential and parallel two-place buffer.
 The corresponding LTSs are:



Assuming $B_0 \sim B'_0$, it then follows that $B_1 \sim B'_2$. However, B'_2 has an outgoing τ -transition while B_1 does not, therefore $B_1 \not\sim B'_2$. From this it follows that the two LTSs are not strongly bisimilar.

Exercise 2

(4 points)

a) $P + \text{nil} \sim P$:

- $P + \text{nil} \xrightarrow{a} P'$ implies $P \xrightarrow{a} P'$ for all $a \in A$ and clearly $P' \sim P'$.
- $P \xrightarrow{a} P'$ obviously implies $P + \text{nil} \xrightarrow{a} P'$ for all $a \in A$ and $P' \sim P'$.

Hence $P + \text{nil} \sim P$.

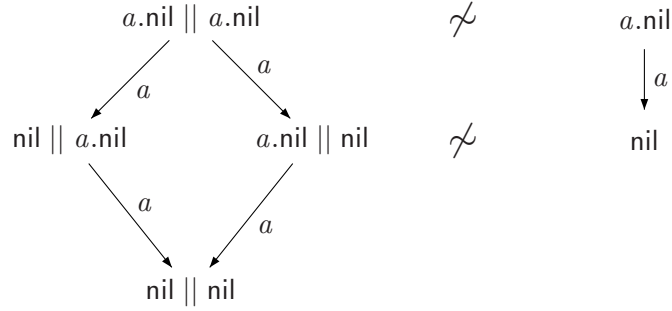
b) $P + P \sim P$:

- $P + P \xrightarrow{a} P'$ implies $P \xrightarrow{a} P'$ for all $a \in A$ and clearly $P' \sim P'$.
- $P \xrightarrow{a} P'$ obviously implies $P + P \xrightarrow{a} P'$ for all $a \in A$ and $P' \sim P'$.

Hence $P + P \sim P$.

c) $P \parallel (Q \parallel R) \sim (P \parallel Q) \parallel R$: Choose $\rho = \{(P \parallel (Q \parallel R), (P \parallel Q) \parallel R) \mid P, Q, R \in \text{Proc}\}$. ρ is a strong bisimulation.

d) $P \parallel P \not\sim P$: A counterexample is the process definition $P = a.\text{nil}$. The two induced LTS are as follows:



Exercise 3

(3 points)

a) Problem: “race conditions” between external user actions and internal τ -steps

Intended:

$$\begin{aligned}
 & \text{new } \vec{\alpha}(\text{Open}(\vec{a}) \parallel \text{Unlocked}(\vec{b}) \parallel \text{Key}(\vec{c})) \\
 & \xrightarrow{\text{close}} \text{new } \vec{\alpha}(\text{Closed}(\vec{a}) \parallel \text{Unlocked}(\vec{b}) \parallel \text{Key}(\vec{c})) \\
 & \xrightarrow{\text{open}} \text{new } \vec{\alpha}((\text{isLocked}.\text{Closed}(\vec{a}) + \text{isUnlocked}.\text{Open}(\vec{a})) \parallel \text{Unlocked}(\vec{b}) \parallel \text{Key}(\vec{c})) \\
 & \xrightarrow{\tau} \text{new } \vec{\alpha}(\text{Open}(\vec{a}) \parallel \text{Unlocked}(\vec{b}) \parallel \text{Key}(\vec{c})) \\
 & \longrightarrow \dots
 \end{aligned}$$

But:

$$\begin{aligned}
& \text{new } \vec{\alpha}((isLocked.Closed(\vec{a}) + isUnlocked.Open(\vec{a})) || Unlocked(\vec{b}) || Key(\vec{c})) \\
& \xrightarrow{pressed} \text{new } \vec{\alpha}((isLocked.Closed(\vec{a}) + isUnlocked.Open(\vec{a})) \\
& \quad || Unlocked(\vec{b}) || \overline{activate}.Key(\vec{c})) \\
& \xrightarrow{\tau} \text{new } \vec{\alpha}((isLocked.Closed(\vec{a}) + isUnlocked.Open(\vec{a})) \\
& \quad || (isOpen.\overline{alarm}.Unlocked(\vec{b}) + isClosed.Locked(\vec{b})) || Key(\vec{c})) \\
& \xrightarrow{pressed} \text{new } \vec{\alpha}((isLocked.Closed(\vec{a}) + isUnlocked.Open(\vec{a})) \\
& \quad || (isOpen.\overline{alarm}.Unlocked(\vec{b}) + isClosed.Locked(\vec{b})) || \overline{activate}.Key(\vec{c}))
\end{aligned}$$

$\Rightarrow$ deadlock situation

b) Idea: Introduce an additional synchronization process

$$\begin{aligned}
Sync(\vec{d}) = & \underbrace{OPEN}_{\text{user action}} . \underbrace{\overline{open}}_{\text{internal action}} . \underbrace{ack}_{\text{acknowledgement}} . Sync(\vec{d}) + \\
& CLOSE.\overline{close}.ack.Sync(\vec{d}) + \\
& PRESS.\overline{pressed}.ack.Sync(\vec{d})
\end{aligned}$$

$\overline{ack}$ has to be introduced in other processes of the system:

$$\begin{aligned}
Door(\vec{a}) &= Open(\vec{a}) \\
Open(\vec{a}) &= \overline{isOpen}.Open(\vec{a}) + close.\overline{ack}.Closed(\vec{a}) + open.\overline{ack}.Open(\vec{a}) \\
Closed(\vec{a}) &= \overline{isClosed}.Closed(\vec{a}) + \\
& \quad open.(isLocked.\overline{ack}.Closed(\vec{a}) + isUnlocked.\overline{ack}.Open(\vec{a})) + close.\overline{ack}.Closed(\vec{a})
\end{aligned}$$

$$\begin{aligned}
Locker(\vec{b}) &= Unlocked(\vec{b}) \\
Unlocked(\vec{b}) &= \overline{isUnlocked}.Unlocked(\vec{b}) + \\
& \quad activate.(isOpen.\overline{ALARM}.ack.Unlocked(\vec{b}) + isClosed.Locked(\vec{b})) \\
Locked(\vec{b}) &= \overline{isLocked}.Locked(\vec{b}) + activate.\overline{ack}.Unlocked(\vec{b})
\end{aligned}$$

$$Key(\vec{c}) = \overline{pressed}.activate.Key(\vec{c})$$

$$System(\vec{e}) = \text{new } \vec{d} (Door(\vec{a}) || Locker(\vec{b}) || Key(\vec{c}) || Sync(\vec{d}))$$

where $\vec{d} = open, close, ack, pressed, isOpen, isClosed, isLocked, isUnlocked, activate$.

Exercise 4

(1 + 3 points)

$P \in Proc$ **well terminating**, if for every $P \rightarrow^* P'$,

$$(*) \left\{ \begin{array}{l} (i) \quad P' \not\stackrel{done}{\rightarrow} \quad \text{and} \\ (ii) \quad P' \stackrel{done}{\rightarrow} \Rightarrow P' \sim \overline{done}.nil \end{array} \right\}$$

a)

nil well terminating: trivial

Let $P_1, P_2 \in Proc$ be well terminating.

$\alpha.P_1 (\alpha \notin \{done, \overline{done}\})$ w.t.:

Let $\alpha.P_1 \rightarrow^n P' \quad (n \geq 0)$

If $n = 0 : P' = \alpha.P_1 \stackrel{done, \overline{done}}{\not\rightarrow} (*)$

If $n \geq 1 : P_1 \rightarrow^{n-1} P' \Rightarrow (*)$

$P_1 + P_2$ w.t.:

Let $P_1 + P_2 \rightarrow^* P'$

$\Rightarrow P_1 \rightarrow^* P'$ or $P_2 \rightarrow^* P'$

$\Rightarrow (*)$

$\text{new } \vec{\alpha} P_1$ w.t.:

Let $\text{new } \vec{\alpha} P_1 \rightarrow^* P'$

$\Rightarrow$ ex. Q' with $P' = \text{new } \vec{\alpha} Q'$ and $P_1 \rightarrow^* Q'$

$\Rightarrow (*)$ applies to Q'

$\Rightarrow (*)$ applies to P' (even if $done \in \vec{\alpha}$)

Well-termination is **not** preserved by parallel composition:

$P = \overline{done}.nil$

but $P \parallel P \xrightarrow{\overline{done}} nil \parallel \overline{done}.nil \xrightarrow{\overline{done}} nil \parallel nil$

$\Rightarrow \overline{done}.nil \parallel \overline{done}.nil \not\sim \overline{done}.nil$

b)

$Done = \overline{done}.nil$

$P \text{ Seq } Q = \text{new } d(P[done \mapsto d] \parallel d.Q)$

$P \text{ Par } Q = \text{new } d_1, d_2(P[done \mapsto d_1] \parallel Q[done \mapsto d_2]) \parallel d_1.d_2.Done$

Seq, Par preserve well-termination:

Induction on the length of the computation