

# Semantics and Verification of Software

## Lecture 11: Axiomatic Semantics of WHILE

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- 1 Repetition: Correctness of Hoare Logic
- 2 Equivalence of Axiomatic and Operational/Denotational Semantics
- 3 Total Correctness

## Theorem (Soundness of Hoare Logic)

For every partial correctness property  $\{A\} c \{B\}$ ,

$$\vdash \{A\} c \{B\} \implies \models \{A\} c \{B\}.$$

## Theorem (Gödel's Incompleteness Theorem)

The set of all valid assertions

$$\{A \in \text{Assn} \mid \models A\}$$

is not recursively enumerable, i.e., there exists no proof system for Assn in which all valid assertions are systematically derivable.

## Theorem (Cook's Completeness Theorem)

Hoare Logic is *relatively complete*, i.e., for every partial correctness property  $\{A\} c \{B\}$ :

$$\models \{A\} c \{B\} \implies \vdash \{A\} c \{B\}.$$

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# Operational/Denotational Equivalence

Def. 4.3:  $\mathfrak{O}[\cdot] : Cmd \rightarrow (\Sigma \rightarrow \Sigma)$  given by

$$\mathfrak{O}[c](\sigma) = \sigma' \iff \langle c, \sigma \rangle \rightarrow \sigma'$$

Def. 4.4: Two statements  $c_1, c_2 \in Cmd$  are called **operationally equivalent** (notation:  $c_1 \sim c_2$ ) if

$$\mathfrak{O}[c_1] = \mathfrak{O}[c_2].$$

Theorem 7.4: For every  $c \in Cmd$ ,

$$\mathfrak{O}[c] = \mathfrak{C}[c],$$

i.e.,  $\mathfrak{O}[\cdot] = \mathfrak{C}[\cdot]$ .

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# Axiomatic Equivalence I

In the axiomatic semantics, two statements have to be considered equivalent if they are indistinguishable w.r.t. partial correctness properties:

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Two statements  $c_1, c_2 \in Cmd$  are called **axiomatically equivalent** (notation:  $c_1 \approx c_2$ ) if, for all assertions  $A, B \in Assn$ ,

$$\models \{A\} c_1 \{B\} \iff \models \{A\} c_2 \{B\}.$$

## Example 11.2

We show that  $c_1; (c_2; c_3) \approx (c_1; c_2); c_3$ . Let  $A, B \in Assn$ :

$$\begin{aligned} & \models \{A\} c_1; (c_2; c_3) \{B\} \\ \iff & \vdash \{A\} c_1; (c_2; c_3) \{B\} \text{ (Theorem 9.5, 10.3)} \\ \iff & \text{ex. } C_1 \in Assn \text{ such that } \vdash \{A\} c_1 \{C_1\}, \vdash \{C_1\} c_2; c_3 \{B\} \text{ (rule (seq))} \\ \iff & \text{ex. } C_1, C_2 \in Assn \text{ such that } \vdash \{A\} c_1 \{C_1\}, \vdash \{C_1\} c_2 \{C_2\}, \\ & \quad \vdash \{C_2\} c_3 \{B\} \text{ (rule (seq))} \\ \iff & \text{ex. } C_2 \in Assn \text{ such that } \vdash \{A\} c_1; c_2 \{C_2\}, \vdash \{C_2\} c_3 \{B\} \text{ (rule (seq))} \\ \iff & \vdash \{A\} (c_1; c_2); c_3 \{B\} \text{ (rule (seq))} \\ \iff & \models \{A\} (c_1; c_2); c_3 \{B\} \text{ (Theorem 9.5, 10.3)} \end{aligned}$$

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## Theorem 11.3

*Axiomatic and denotational/operational equivalence coincide, i.e., for all  $c_1, c_2 \in Cmd$ ,*

$$c_1 \approx c_2 \iff c_1 \sim c_2.$$

Proof.

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- **Observation:** partial correctness properties only speak about **terminating** computations of a given program
- Total correctness additionally requires the proof that the program indeed stops (on the input states specified by the precondition)
- Consider **total correctness properties** of the form

$$\{A\} c \{\Downarrow B\}$$

where  $c \in Cmd$  and  $A, B \in Assn$

- Interpretation:

Validity of property  $\{A\} c \{\Downarrow B\}$

For all states  $\sigma \in \Sigma$  which satisfy  $A$ :

the execution of  $c$  in  $\sigma$  terminates and yields a state which satisfies  $B$ .

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## Definition 11.4 (Semantics of total correctness properties)

Let  $A, B \in Assn$  and  $c \in Cmd$ .

- $\{A\} c \{\Downarrow B\}$  is called **valid in  $\sigma \in \Sigma$  and  $I \in Int$**  (notation:  $\sigma \models^I \{A\} c \{\Downarrow B\}$ ) if  $\sigma \models^I A$  implies that  $\mathfrak{C}[c]\sigma \neq \perp$  and  $\mathfrak{C}[c]\sigma \models^I B$ .
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# Proving Total Correctness I

**Goal:** syntactic derivation of valid total correctness properties

Definition 11.5 (Hoare Logic for total correctness)

The **Hoare rules** for total correctness are given by

$$\frac{}{\{A\} \text{ skip } \{\Downarrow A\}} \text{ (skip)} \quad \frac{}{\{A[x \mapsto a]\} x := a \{\Downarrow A\}} \text{ (asgn)}$$
$$\frac{\{A\} c_1 \{\Downarrow C\} \quad \{C\} c_2 \{\Downarrow B\}}{\{A\} c_1; c_2 \{\Downarrow B\}} \text{ (seq)} \quad \frac{\{A \wedge b\} c_1 \{\Downarrow B\} \quad \{A \wedge \neg b\} c_2 \{\Downarrow B\}}{\{A\} \text{ if } b \text{ then } c_1 \text{ else } c_2 \{\Downarrow B\}} \text{ (if)}$$
$$\frac{\{i \geq 0 \wedge A(i+1)\} c \{\Downarrow A(i)\}}{\{\exists i. i \geq 0 \wedge A(i)\} \text{ while } b \text{ do } c \{\Downarrow A(0)\}} \text{ (while)}$$
$$\frac{\models (A \implies A') \quad \{A'\} c \{\Downarrow B'\}}{\{A\} c \{\Downarrow B\}} \text{ (cons)}$$

where  $i \in LVar$ ,  $\models (i \geq 0 \wedge A(i+1) \implies b)$ , and  $\models (A(0) \implies \neg b)$ .

A total correctness property is **provable** (notation:  $\vdash \{A\} c \{\Downarrow B\}$ ) if it is derivable by the Hoare rules. In case of (while),  $A(i)$  is called a **(loop) invariant**.

- In rule

$$\frac{\{i \geq 0 \wedge A(i+1)\} c \{\Downarrow A(i)\}}{\{\exists i. i \geq 0 \wedge A(i)\} \text{while } b \text{ do } c \{\Downarrow A(0)\}} \text{ (while)}$$

the notation  $A(i)$  indicates that assertion  $A$  parametrically depends on the value of the logical variable  $i \in LVar$ .

- Idea:  $i$  represents the remaining number of loop iterations
- Execution terminated
  - $\Rightarrow A(0)$  holds
  - $\Rightarrow$  execution condition  $b$  false
- Loop to be traversed  $i+1$  times ( $i \geq 0$ )
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Thus:  $\models (A(0) \Rightarrow \neg b)$

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# Proving Total Correctness II

- In rule

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- Idea:  $i$  represents the **remaining number of loop iterations**

- Execution terminated

$\implies A(0)$  holds

$\implies$  execution condition  $b$  false

Thus:  $\models (A(0) \implies \neg b)$

- Loop to be traversed  $i+1$  times ( $i \geq 0$ )

$\implies A(i+1)$  holds

$\implies$  execution condition  $b$  true

Thus:  $\models (i \geq 0 \wedge A(i+1) \implies b)$ , and  $i+1$  decreased to  $i$  after execution of  $c$

# Proving Total Correctness II

- In rule

$$\frac{\{i \geq 0 \wedge A(i+1)\} c \{\Downarrow A(i)\}}{\{\exists i. i \geq 0 \wedge A(i)\} \text{while } b \text{ do } c \{\Downarrow A(0)\}} \text{ (while)}$$

the notation  $A(i)$  indicates that assertion  $A$  parametrically depends on the value of the logical variable  $i \in LVar$ .

- Idea:  $i$  represents the **remaining number of loop iterations**

- Execution terminated

$\implies A(0)$  holds

$\implies$  execution condition  $b$  false

Thus:  $\models (A(0) \implies \neg b)$

- Loop to be traversed  $i + 1$  times ( $i \geq 0$ )

$\implies A(i+1)$  holds

$\implies$  execution condition  $b$  true

Thus:  $\models (i \geq 0 \wedge A(i+1) \implies b)$ , and  $i + 1$  decreased to  $i$  after execution of  $c$

## Example 11.6

Proof of  $\{A\} y := 1; c \{\downarrow B\}$  where

$$A := (x > 0 \wedge x = i)$$

$c := \text{while } \neg(x=1) \text{ do } (y := y * x; x := x - 1)$

$$B := (y = i!)$$

(on the board)