

Semantics and Verification of Software

Lecture 4: Operational Semantics of WHILE III (Properties of Execution Relation)

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Execution of Statements

Remember:

$c ::= \text{skip} \mid x := a \mid c_1; c_2 \mid \text{if } b \text{ then } c_1 \text{ else } c_2 \mid \text{while } b \text{ do } c \in \text{Cmd}$

Definition (Execution relation for statements)

For $c \in \text{Cmd}$ and $\sigma, \sigma' \in \Sigma$, the **execution relation** $\langle c, \sigma \rangle \rightarrow \sigma'$ is defined by the following rules:

$$(\text{skip}) \frac{}{\langle \text{skip}, \sigma \rangle \rightarrow \sigma}$$

$$(\text{asgn}) \frac{\langle a, \sigma \rangle \rightarrow z}{\langle x := a, \sigma \rangle \rightarrow \sigma[x \mapsto z]}$$

$$(\text{seq}) \frac{\langle c_1, \sigma \rangle \rightarrow \sigma' \quad \langle c_2, \sigma' \rangle \rightarrow \sigma''}{\langle c_1; c_2, \sigma \rangle \rightarrow \sigma''}$$

$$(\text{if-t}) \frac{\langle b, \sigma \rangle \rightarrow \text{true} \quad \langle c_1, \sigma \rangle \rightarrow \sigma'}{\langle \text{if } b \text{ then } c_1 \text{ else } c_2, \sigma \rangle \rightarrow \sigma'}$$

$$(\text{if-f}) \frac{\langle b, \sigma \rangle \rightarrow \text{false} \quad \langle c_2, \sigma \rangle \rightarrow \sigma'}{\langle \text{if } b \text{ then } c_1 \text{ else } c_2, \sigma \rangle \rightarrow \sigma'}$$

$$(\text{wh-f}) \frac{\langle b, \sigma \rangle \rightarrow \text{false}}{\langle \text{while } b \text{ do } c, \sigma \rangle \rightarrow \sigma}$$

$$(\text{wh-t}) \frac{\langle b, \sigma \rangle \rightarrow \text{true} \quad \langle c, \sigma \rangle \rightarrow \sigma' \quad \langle \text{while } b \text{ do } c, \sigma' \rangle \rightarrow \sigma''}{\langle \text{while } b \text{ do } c, \sigma \rangle \rightarrow \sigma''}$$

This operational semantics is well defined in the following sense:

Theorem

*The execution relation for statements is **deterministic**, i.e., whenever $c \in \text{Cmd}$ and $\sigma, \sigma', \sigma'' \in \Sigma$ such that $\langle c, \sigma \rangle \rightarrow \sigma'$ and $\langle c, \sigma \rangle \rightarrow \sigma''$, then $\sigma' = \sigma''$.*

- How to prove this theorem?
- Idea:
 - employ corresponding result for **expressions** (Lemma 3.6)
 - use **induction on the syntactic structure** of c ↴
- Instead: **structural induction on derivation trees**

Proof (Theorem 3.5).

To show:

$$\langle c, \sigma \rangle \rightarrow \sigma', \langle c, \sigma \rangle \rightarrow \sigma'' \implies \sigma' = \sigma''$$

Proof by structural induction on derivation tree for $\langle c, \sigma \rangle \rightarrow \sigma'$.

Already considered:

- (skip) $\frac{}{\langle \text{skip}, \sigma \rangle \rightarrow \sigma}$ (i.e., $c = \text{skip}$, $\sigma' = \sigma$):

since this axiom is the only applicable derivation rule, it follows that also $\sigma'' = \sigma = \sigma'$.

- (asgn) $\frac{\langle a, \sigma \rangle \rightarrow z}{\langle x := a, \sigma \rangle \rightarrow \sigma[x \mapsto z]}$ (i.e., $c = (x := a)$, $\sigma' = \sigma[x \mapsto z]$):

here the second derivation must be of the form

$$\frac{\text{(asgn)} \quad \langle a, \sigma \rangle \rightarrow z'}{\langle x := a, \sigma \rangle \rightarrow \sigma[x \mapsto z']}$$

such that Lemma 3.6(1) implies $z = z'$, and hence

$$\sigma'' = \sigma[x \mapsto z'] = \sigma[x \mapsto z] = \sigma'.$$

- ... (on the board)

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Functional of the Operational Semantics

The determinism of the execution relation (Theorem 3.5) justifies the following definition:

Definition 4.1 (Operational functional)

The **functional of the operational semantics**,

$$\mathfrak{O}[\cdot] : Cmd \rightarrow (\Sigma \dashrightarrow \Sigma),$$

assigns to every statement $c \in Cmd$ a partial state transformation

$\mathfrak{O}[c] : \Sigma \dashrightarrow \Sigma$, which is defined as follows:

$$\mathfrak{O}[c]\sigma := \begin{cases} \sigma' & \text{if } \langle c, \sigma \rangle \rightarrow \sigma' \text{ for some } \sigma' \in \Sigma \\ \text{undefined} & \text{otherwise} \end{cases}$$

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Remark: $\mathfrak{O}[c]\sigma$ can indeed be undefined

(consider e.g. $c = \text{while true do skip}$; see Corollary 3.4)

Equivalence of Statements

Underlying principle: two (syntactic) objects are considered (semantically) **equivalent** if they have the same “meaning”

- finite automata: $A_1 \sim A_2$ iff $L(A_1) = L(A_2)$
- context-free grammars: $G_1 \sim G_2$ iff $L(G_1) = L(G_2)$
- Turing machines: $T_1 \sim T_2$ iff both compute same function

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Definition 4.2 (Operational equivalence)

Two statements $c_1, c_2 \in \text{Cmd}$ are called **(operationally) equivalent** (notation: $c_1 \sim c_2$) iff

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Thus:

- $c_1 \sim c_2$ iff $\mathfrak{O}[c_1]\sigma = \mathfrak{O}[c_2]\sigma$ for every $\sigma \in \Sigma$
- In particular, $\mathfrak{O}[c_1]\sigma$ is undefined iff $\mathfrak{O}[c_2]\sigma$ is undefined

Simple application of statement equivalence: test of execution condition in a `while` loop can be represented by an `if` statement

Lemma 4.3

For every $b \in BExp$ and $c \in Cmd$,

`while b do c` \sim `if b then (c;while b do c) else skip.`

“Unwinding” of Loops

Simple application of statement equivalence: test of execution condition in a `while` loop can be represented by an `if` statement

Lemma 4.3

For every $b \in BExp$ and $c \in Cmd$,

$\text{while } b \text{ do } c \sim \text{if } b \text{ then } (c; \text{while } b \text{ do } c) \text{ else skip.}$

Proof.

on the board

