

Semantics and Verification of Software

Lecture 20: Nondeterminism and Parallelism V (Wrap-Up)

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Lehrstuhl für Informatik 2
(Software Modeling and Verification)



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<http://www-i2.informatik.rwth-aachen.de/i2/svsw13/>

Summer Semester 2013

Online Registration for Seminars and Practical Courses (Praktika) in Winter Term 2013/14

Who?

Students of: • Master Courses
• Bachelor Informatik (~~Pro~~Seminar!)

Where?

www.graphics.rwth-aachen.de/apse

When?

05.07.2013 - 17.07.2013

- 1 Recapitulation: Calculus of Communicating Systems
- 2 Decidability of Strong Bisimulation
- 3 Definition of Weak Bisimulation
- 4 Summary: Nondeterminism and Concurrency
- 5 Further Topics in Formal Semantics
- 6 Miscellaneous

Definition (Semantics of CCS)

A process definition $(A_i(a_{i1}, \dots, a_{in_i}) = P_i \mid 1 \leq i \leq k)$ determines the **labeled transition system (LTS)** (Prc, Act, \rightarrow) whose transitions can be inferred from the following rules ($P, P', Q, Q' \in Prc$, $\alpha \in Act$, $\lambda \in N \cup \bar{N}$, $a, b \in N$, $A \in Pid$):

$$(Act) \frac{}{\alpha.P \xrightarrow{\alpha} P}$$

$$(Sum_1) \frac{P \xrightarrow{\alpha} P'}{P + Q \xrightarrow{\alpha} P'}$$

$$(Par_1) \frac{P \xrightarrow{\alpha} P'}{P \parallel Q \xrightarrow{\alpha} P' \parallel Q}$$

$$(New) \frac{P \xrightarrow{\alpha} P' \ (\alpha \notin \{a, \bar{a}\})}{\text{new } a P \xrightarrow{\alpha} \text{new } a P'}$$

$$(Com) \frac{P \xrightarrow{\lambda} P' \ Q \xrightarrow{\bar{\lambda}} Q'}{P \parallel Q \xrightarrow{\tau} P' \parallel Q'}$$

$$(Sum_2) \frac{Q \xrightarrow{\alpha} Q'}{P + Q \xrightarrow{\alpha} Q'}$$

$$(Par_2) \frac{Q \xrightarrow{\alpha} Q'}{P \parallel Q \xrightarrow{\alpha} P \parallel Q'}$$

$$(Call) \frac{P[\vec{a} \mapsto \vec{b}] \xrightarrow{\alpha} P'}{A(\vec{b}) \xrightarrow{\alpha} P'} \text{ if } A(\vec{a}) = P$$

(Here $P[\vec{a} \mapsto \vec{b}]$ denotes the replacement of every a_i by b_i in P .)

Definition of Strong Bisimulation

Definition (Strong bisimulation)

A relation $\rho \subseteq Prc \times Prc$ is called a **strong bisimulation** if $P\rho Q$ implies, for every $\alpha \in Act$,

- ① $P \xrightarrow{\alpha} P' \Rightarrow \text{ex. } Q' \in Prc \text{ such that } Q \xrightarrow{\alpha} Q' \text{ and } P' \rho Q'$
- ② $Q \xrightarrow{\alpha} Q' \Rightarrow \text{ex. } P' \in Prc \text{ such that } P \xrightarrow{\alpha} P' \text{ and } P' \rho Q'$

$P, Q \in Prc$ are called **strongly bisimilar** (notation: $P \sim Q$) if there exists a strong bisimulation ρ such that $P\rho Q$.

Theorem

\sim is an equivalence relation.

Proof.

omitted



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We now show that the word problem for strong bisimulation

Problem (Word problem for strong bisimulation)

Given: $P, Q \in Prc$

Question: $P \sim Q$?

is decidable for finite-state processes (i.e., for those with $|Prc(P)|, |Prc(Q)| < \infty$ where $Prc(P) := \{P' \in Prc \mid P \longrightarrow P'\}$)
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To this aim we give an algorithm which iteratively partitions the state set of an LTS such that the single blocks correspond to the \sim -equivalence classes.

The Partitioning Algorithm I

Theorem 20.1 (Partitioning algorithm for \sim)

Input: $LTS (S, Act, \rightarrow)$ (S finite)

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Then, for every $P, Q \in S$,

$$P \sim Q \iff \text{ex. } B \in \hat{\Pi} \text{ with } P, Q \in B$$

Remark: if states from two disjoint LTSs $(S_1, Act_1, \rightarrow_1)$ and $(S_2, Act_2, \rightarrow_2)$ (where $S_1 \cap S_2 = \emptyset$) are to be compared, their union $(S_1 \cup S_2, Act_1 \cup Act_2, \rightarrow_1 \cup \rightarrow_2)$ is chosen as input (here usually $Act_1 = Act_2$)

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Example 20.2

Binary semaphore (on the board)

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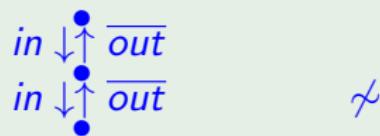
Inadequacy of Strong Bisimulation

Observation: requirement of **exact matching** sometimes too strong

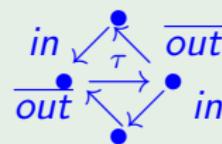
Example 20.3

Sequential and parallel two-place buffer:

$$\begin{array}{ll} B_0(in, out) = in.B_1(in, out) & B_{\parallel}(in, out) = \text{new com } (B(in, com) \parallel \\ B_1(in, out) = \overline{out}.B_0(in, out) + & \quad \quad \quad B(com, out)) \\ \quad \quad \quad in.B_2(in, out) & B(in, out) = in.\overline{out}.B(in, out) \\ B_2(in, out) = \overline{out}.B_1(in, out) & \end{array}$$



↗



Idea: abstract from silent actions

Definition 20.4

- Given $w \in Act^*$, $\hat{w} \in (N \cup \bar{N})^*$ denotes the sequence of non- τ -actions in w (in particular, $\widehat{\tau^n} = \varepsilon$ for every $n \in \mathbb{N}$).

Definition of Weak Bisimulation I

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$$P \xrightarrow{w} Q \iff P \xrightarrow{(\tau \rightarrow)^*} \xrightarrow{\alpha_1} \xrightarrow{(\tau \rightarrow)^*} \dots \xrightarrow{\alpha_n} \xrightarrow{(\tau \rightarrow)^*} Q$$

(and hence: $\xrightarrow{\varepsilon} = (\tau \rightarrow)^*$).

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- A relation $\rho \subseteq Prc \times Prc$ is called a **weak bisimulation** if $P\rho Q$ implies, for every $\alpha \in Act$,
 - $P \xrightarrow{\alpha} P' \Rightarrow \exists Q' \in Prc \text{ such that } Q \xrightarrow{\widehat{\alpha}} Q' \text{ and } P' \rho Q'$
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Remark: each of the two clauses in the definition of weak bisimulation subsumes **two cases**:

- $P \xrightarrow{\alpha} P'$ where $\alpha \neq \tau$
⇒ ex. $Q' \in Prc$ such that $Q (\xrightarrow{\tau})^* \xrightarrow{\alpha} (\xrightarrow{\tau})^* Q'$ and $P' \rho Q'$
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Example 20.5

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- Classical “**Input → Output**” view not sufficient
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 - alternative approach: “**true**” **concurrency**
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- Requires new notions of program/process equivalence (**bisimulation**)

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- Simplest setting: **first-order** function definitions of the form
$$f(x_1, \dots, x_n) = t$$
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- **Operational semantics** (only function calls)
 - **call-by-value** case:

$$\frac{t_1 \rightarrow z_1 \quad \dots \quad t_n \rightarrow z_n \quad t[x_1 \mapsto z_1, \dots, x_n \mapsto z_n] \rightarrow z}{f(t_1, \dots, t_n) \rightarrow z}$$

- **call-by-name** case:

$$\frac{t[x_1 \mapsto t_1, \dots, x_n \mapsto t_n] \rightarrow z}{f(t_1, \dots, t_n) \rightarrow z}$$

- Denotational semantics

- program = **equation system** (for functions)
- induces call-by-value and call-by-name **functional**
- **monotonic and continuous** w.r.t. graph inclusion
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- see [Winskel 1996, Sct. 9] and **Functional Programming** course [Giesl]

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 - Mon 22 July – Fri 26 July
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Just drop me a mail!

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- Teaching in Winter 2013/14:

- Course **Introduction to Model Checking** [Katoen]
- Course **Concurrency Theory** [Katoen/Noll]
- Seminar **Trends in Computer-Aided Verification** [Katoen/Noll/NN]