

Testing of Reactive Systems

Lecture 4: Implementation Relations, Observing Behaviour

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Summer Semester 2009

What happened so far?

- SOS of processes
- Preorders, Equivalences
- Trace preorder
- Bisimulation equivalence

Definition Bisimulation Equivalence

A **bisimulation** is a binary relation $R \subseteq \mathbb{P} \times \mathbb{P}$ satisfying:

For all $a \in Act$:

- 1) if pRq and $p \xrightarrow{a} p'$, then $\exists q' \in \mathbb{P} : q \xrightarrow{a} q'$ and $p'Rq'$.
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Lifting bisimulation to $\xrightarrow{\sigma}$

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Proof:

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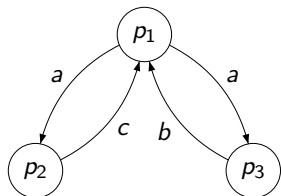
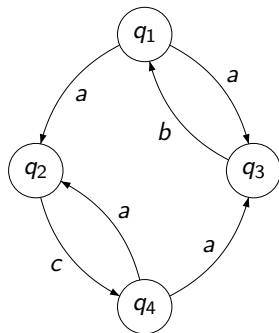
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Example 2.2.6

 $L_1 :$  $L_2 :$ 

Comparing preorders

- A preorder is a set
- We can relate different preorders by **set inclusion**.

Definition 2.2.7 (Finer and Coarser)

Let \leq, \leq' be preorders and $\leq \subseteq \leq'$.

We say

- 1 \leq is **finer** than \leq' (written $\leq \preceq \leq'$),
- 2 \leq' is **coarser** than \leq .

Analogously for equivalences.

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Finer and Coarser

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Finer preorder:

- 1 is more “picky”
- 2 distinguishes more processes from each other
- 3 **equivalences**: the partitioning is finer
- 4 \leq finer than \leq' means:

$$p \leq q \quad \text{implies} \quad p \leq' q$$

- 5 We write $pre_1 \preceq pre_2$ iff pre_1 is finer than pre_2 .

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Comparing Trace and Bisimulation Equivalence

Proposition 2.2.8

\sim_{tr} is coarser than \sim_B , but not finer.

Proof:

\Rightarrow Blackboard

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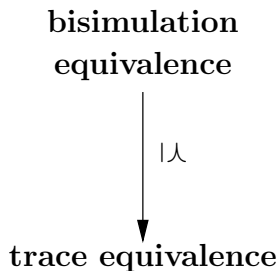
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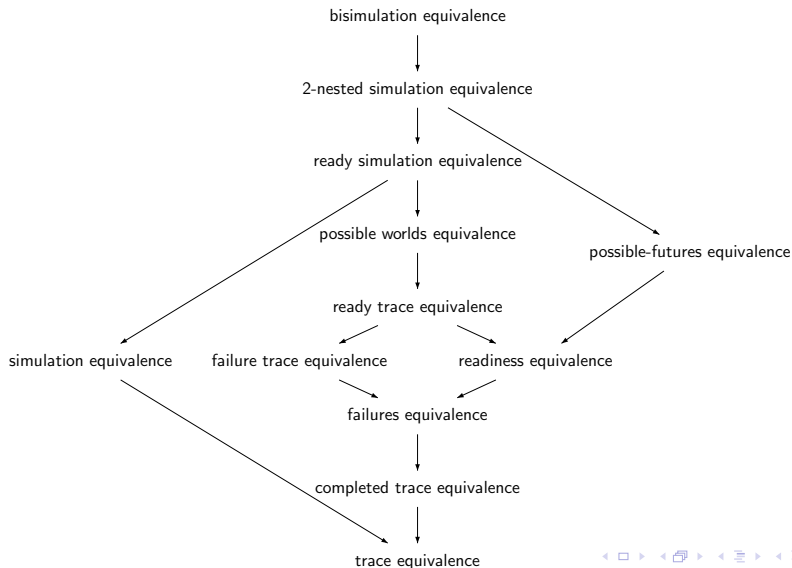
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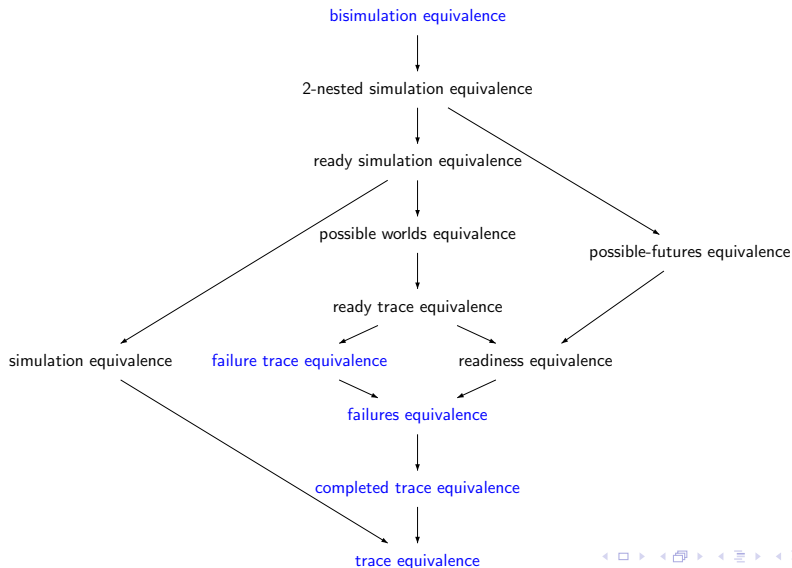
The picture so far



The whole picture: linear time – branching time spectrum



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Determinism and the LTBT spectrum

Let \mathbb{IP}_{det} be the set of all **deterministic processes**.

Proposition 2.3.1

If we restrict \sim_B and \sim_{tr} on \mathbb{IP}_{det} , then

$$\sim_B = \sim_{tr}$$

Restricted means, we look at:

- $\sim_B \cap \mathbb{IP}_{\text{det}} \times \mathbb{IP}_{\text{det}}$
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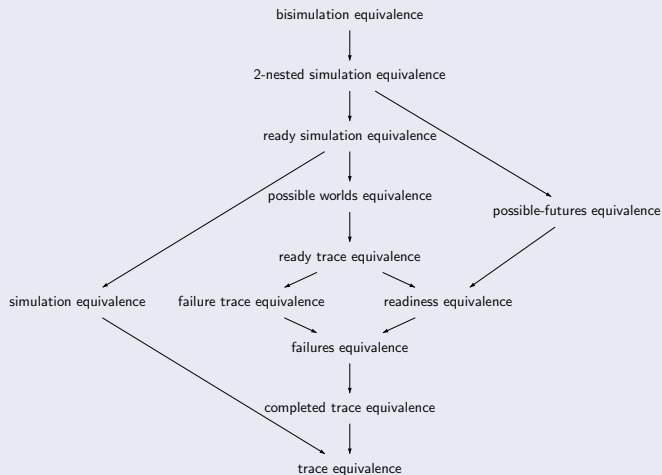
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Implications

Collapse of the linear time – branching time spectrum



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bisimulation equivalence
2-nested simulation equivalence
possible-futures equivalence
ready simulation equivalence
possible worlds equivalence
simulation equivalence
ready trace equivalence
readiness equivalence
failure trace equivalence
failures equivalence
completed trace equivalence
trace equivalence

Implications

Differences between equivalences

The equivalences differ in the way **how they treat nondeterminism in processes**

Implications

Position in the diagram

... is an indication on **how much nondeterminism is taken into account** to distinguish processes

Implications

Trace equivalence

... **ignores non-determinism** completely

Bisimulation equivalence

... takes **most** of the information on nondeterminism into account

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Why linear-time - branching-time?

- trace equivalence works on traces, linear sequences of actions
⇒ “linear time”
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Proof idea

To show: 1) $\sim_B \subseteq \sim_{tr}$ and 2) $\sim_{tr} \subseteq \sim_B$

1) is already clear, since \sim_B is finer than \sim_{tr}

2) We assume

- $p, q \in \mathbb{P}_{\text{det}}$
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Approach: construct a bisimulation R such that $p R q$.

This implies $p \sim_B q$

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Summary Part 2

- Preorders, equivalences
- Trace inclusion
- Bisimulation equivalence
- Comparison of preorders, the LTBT spectrum
- The collapse of the LTBT spectrum for deterministic processes

Part 3: Distinguishing Processes by Manipulation and Observation

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How can we relate *implementation relations* to testing?

Testing has much of **experimenting**:

- test-object is manipulated
- reactions are observed

How to compare the behaviour of **processes** (our test-objects) by **manipulation** and **observation**?

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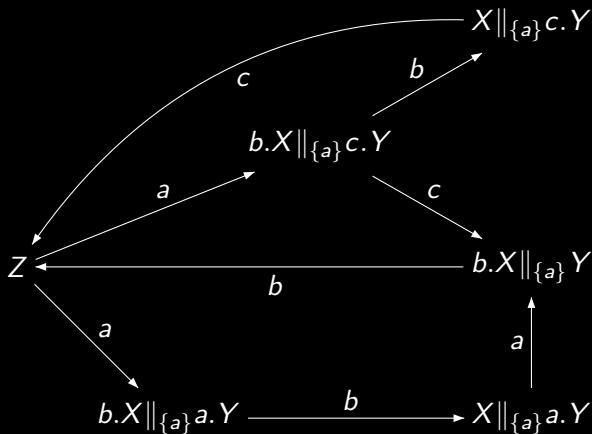
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Framework

Process in black-box



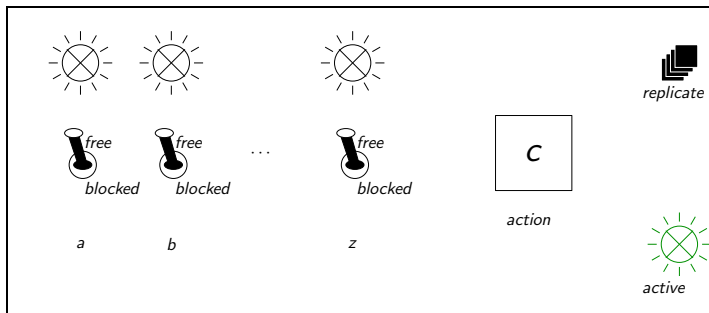
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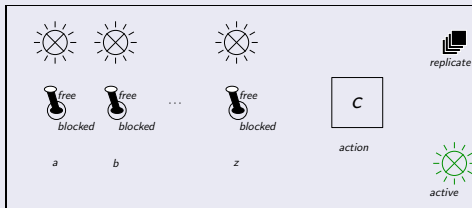
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Manipulating and Observing Processes: the User Panel



The Generative machine

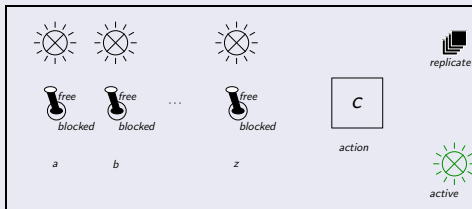
User panel



- Process in black box executes actions spontaneously
- Gadgets on user panel: possible observations
- Display: current action
- Switches: restricting corresponding action
- Menu Lights: actions that can be executed

The Generative machine

User panel

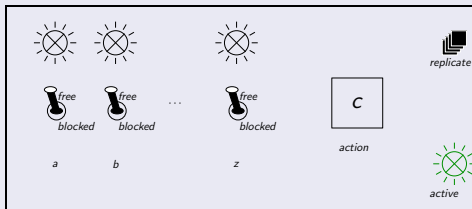


Convention

- Switches **manipulate** process
- Possible manipulation can be seen as **observation**
- Convention: **manipulations are observations**

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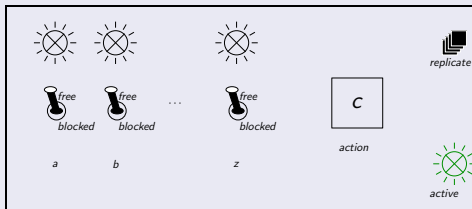


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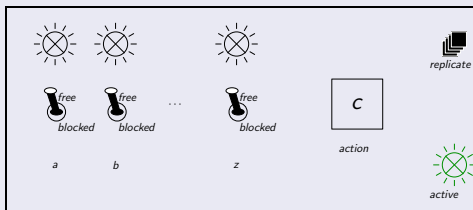


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What are observations?

User panel

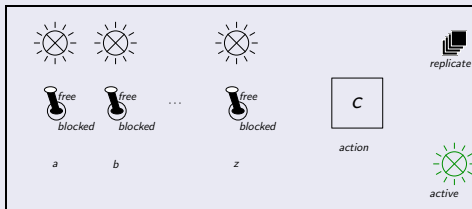


Display

- shows the action that is currently executed

What are observations?

User panel

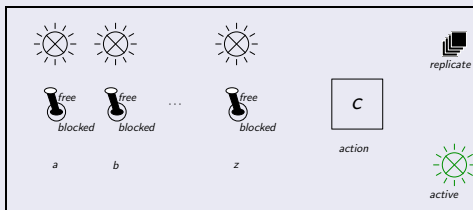


Switches

- set on **free**: action can be executed
- set on **blocked**: action can not be executed
- machine **idles**, if no free action can be executed, **display is empty**
- all switches on free, display empty: **machine is deadlocked**

What are observations?

User panel



Menus

- For each action one light
- Machine idle: light on for actions that can be executed
- Set of lighted actions: [menu](#)

Observations

Primitive observations \mathcal{PO}

- 1 Actions: $Act \subseteq \mathcal{PO}$
- 2 Refusals: $\tilde{X} \in \mathcal{PO}$, for all $X \in 2^{Act}$
actions $a \in X$ are **set on free**
- 3 Menus: $(X) \in \mathcal{PO}$, for all $X \in 2^{Act}$
actions $a \in X$ **can be executed**

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Sequences $\sigma \in \mathcal{PO}^*$ are our **observations**

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Observations and implementation relations

Given:

- processes p, q inside black box
- $\text{obs}(p), \text{obs}(q) \subseteq \mathcal{PO}^*$: the sequences of observations of p and q
- the elements of $\text{obs}(\cdot)$ depend on the primitive observation chosen

Implementation relations

- $p \leq^{\text{may}} q$ iff $\text{obs}(p) \subseteq \text{obs}(q)$
- $p \leq^{\text{must}} q$ iff $\text{obs}(p) \supseteq \text{obs}(q)$

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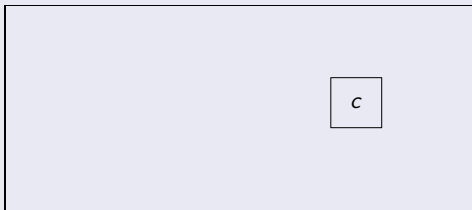
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Trace preorder

The panel

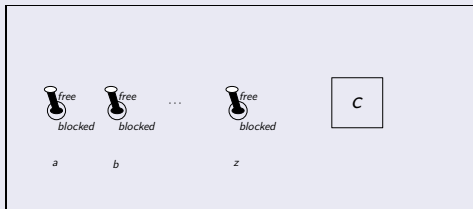


Observations and preorder

- $\text{obs}_{tr}(p)$: the sequence of actions executed by p
- $p \leq_{tr} q$ iff $\text{obs}_{tr}(p) \subseteq \text{obs}_{tr}(q)$

Failure preorder

Switches and display



Observations

- Switches on **free** or **blocked**, **blocked** actions not executable.
- $X \subseteq Act$: actions set on **free**.
- deadlock: machine **refuses** X
- observations: pairs $\langle \sigma, X \rangle$: trace σ leads up to refusal X

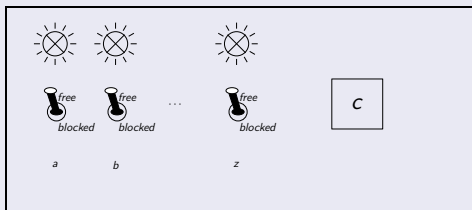
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Preorder

- $\langle \sigma, X \rangle$: failure pair
- $\text{obs}_f(p) = \{ \langle \sigma, X \rangle \mid p \text{ executes } \sigma, \text{ then refuses } X \}$
- $p \leq_f q$ iff $\text{obs}_f(p) \subseteq \text{obs}_f(q)$
- failure preorder

Readiness preorder

Switches and display



Observations

- Situation as for failures preorder
- If machine idles: menu lights indicate which actions could have continued
- $X \subseteq Act$: $\langle \sigma, X \rangle$ ready pair of p iff

$$\exists p' : p \xrightarrow{\sigma} p' \text{ and } p' \xrightarrow{a} \forall a \in X.$$

Failure trace and Readiness trace preorder

Failures scenario and readiniess scenario

- Observation **stops** after machine idles
- machine is **reset** to starting state
- **new observation** starts

Alternative

- after machine idles **continue observing**
- **unlock** machine by setting switches to **free**
- **Observations:**
 - $\sigma \in (\{\tilde{X} \mid X \in 2^{Act}\} \cup Act)^*$: **failure traces**
 - $\sigma \in (\{(X) \mid X \in 2^{Act}\} \cup Act)^*$: **ready traces**
- **failure trace** preorder, **ready trace** preorder

More Gadgets

Green light



Light is off: process **idle** or **deadlocked**

Light is on: process active with action

internal: if display is empty

observable: if display shows name

- Allows to distinguish deadlock and internal activity
- useless if process has no τ transitions

More Gadgets

Replicate Button

- if pressed, process (in its current state) is replicated:
 - one or finitely many copies
 - infinite nr. of copies (only interesting for systems with infinite branching)
 - observation of different futures
 - \implies trees rather than traces
 - necessary to characterise bisimulation

What is coming?

We will

- formalise notion of (primitive) observation
- introduce so-called **observers**
- observers will be **special processes**: test expressions
- observers will **manipulate** and **observe**, i.e.: test
- characterise a few simple preorders by **observers**
- establish order in LTBT spectrum

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